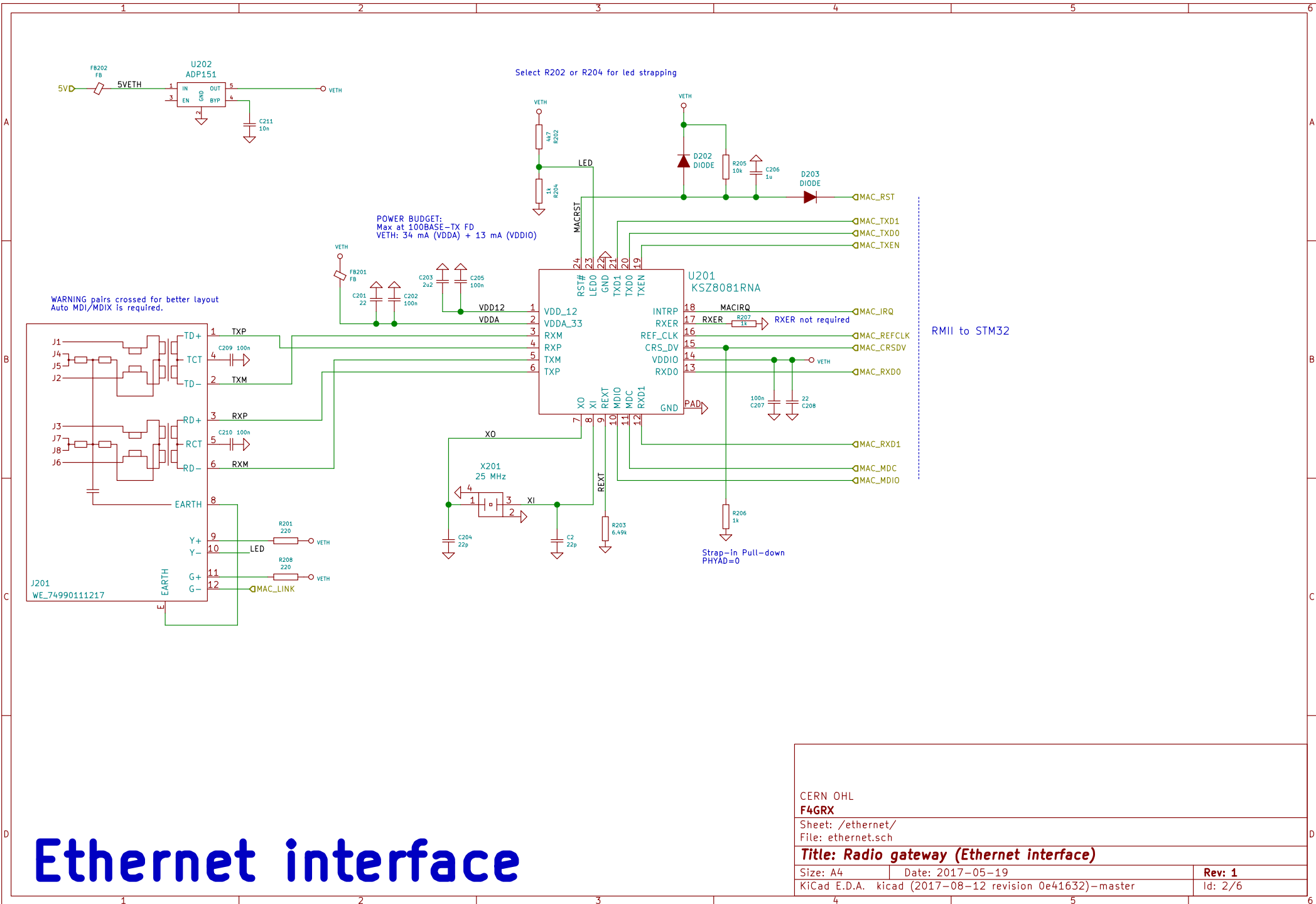


CERN OHL
F4GRX

Sheet: /
 File: ap.sch

Title: Radio gateway (overview)

Size: A4	Date: 2017-05-19	Rev: 1
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Ethernet interface

CERN OHL
F4GRX

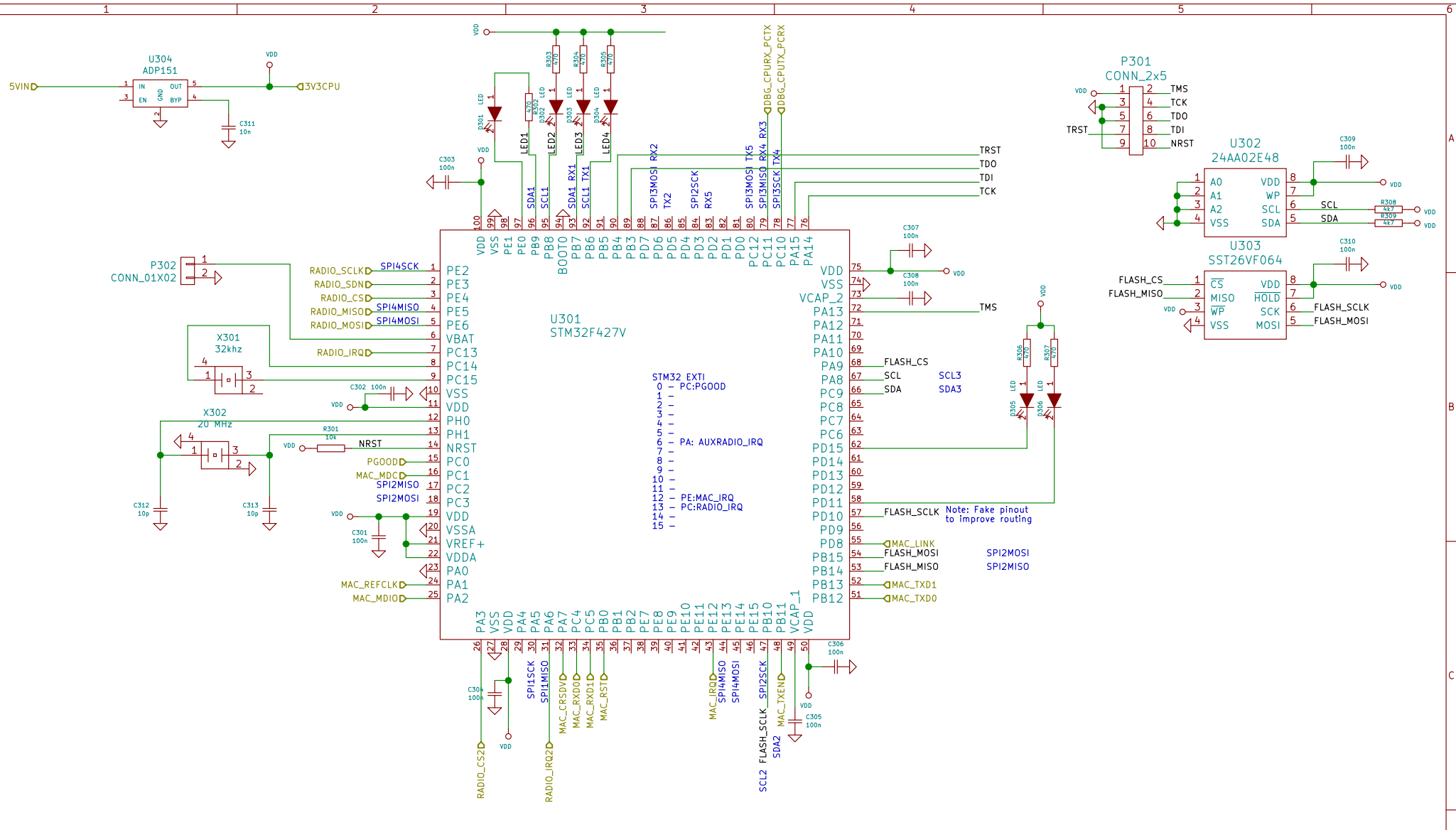
Sheet: /ethernet/
 File: ethernet.sch

Title: Radio gateway (Ethernet interface)

Size: A4 Date: 2017-05-19
 KiCad E.D.A. kicad (2017-08-12 revision 0e41632)-master

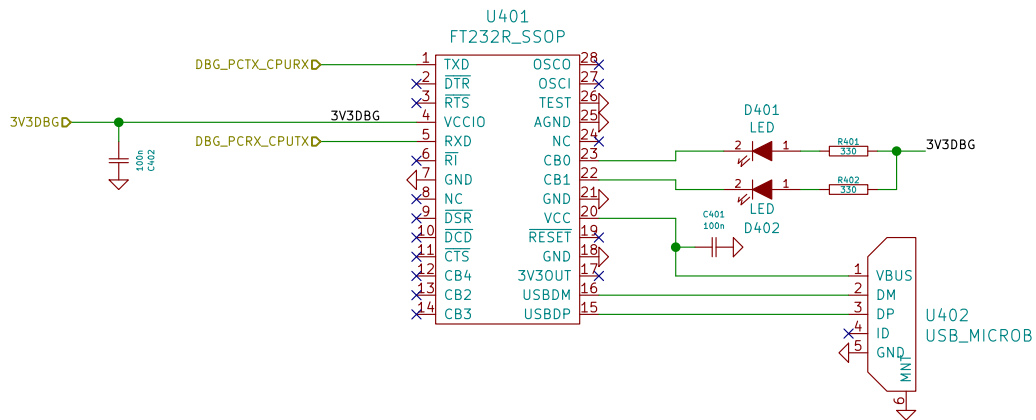
Rev: 1
 Id: 2/6

CPU



CERN OHL	
F4GRX	
Sheet: /cpu/	
File: cpu.sch	
Title: Radio gateway (CPU)	
Size: A4	Date: 2017-05-19
KiCad E.D.A. kicad (2017-08-12 revision 0e41632)-master	
	Rev: 1
	Id: 3/6

DEBUG



CERN OHL
F4GRX

Sheet: /debug/
File: debug.sch

Title: Radio gateway (Debug UART)

Size: A4 Date: 2017-05-19

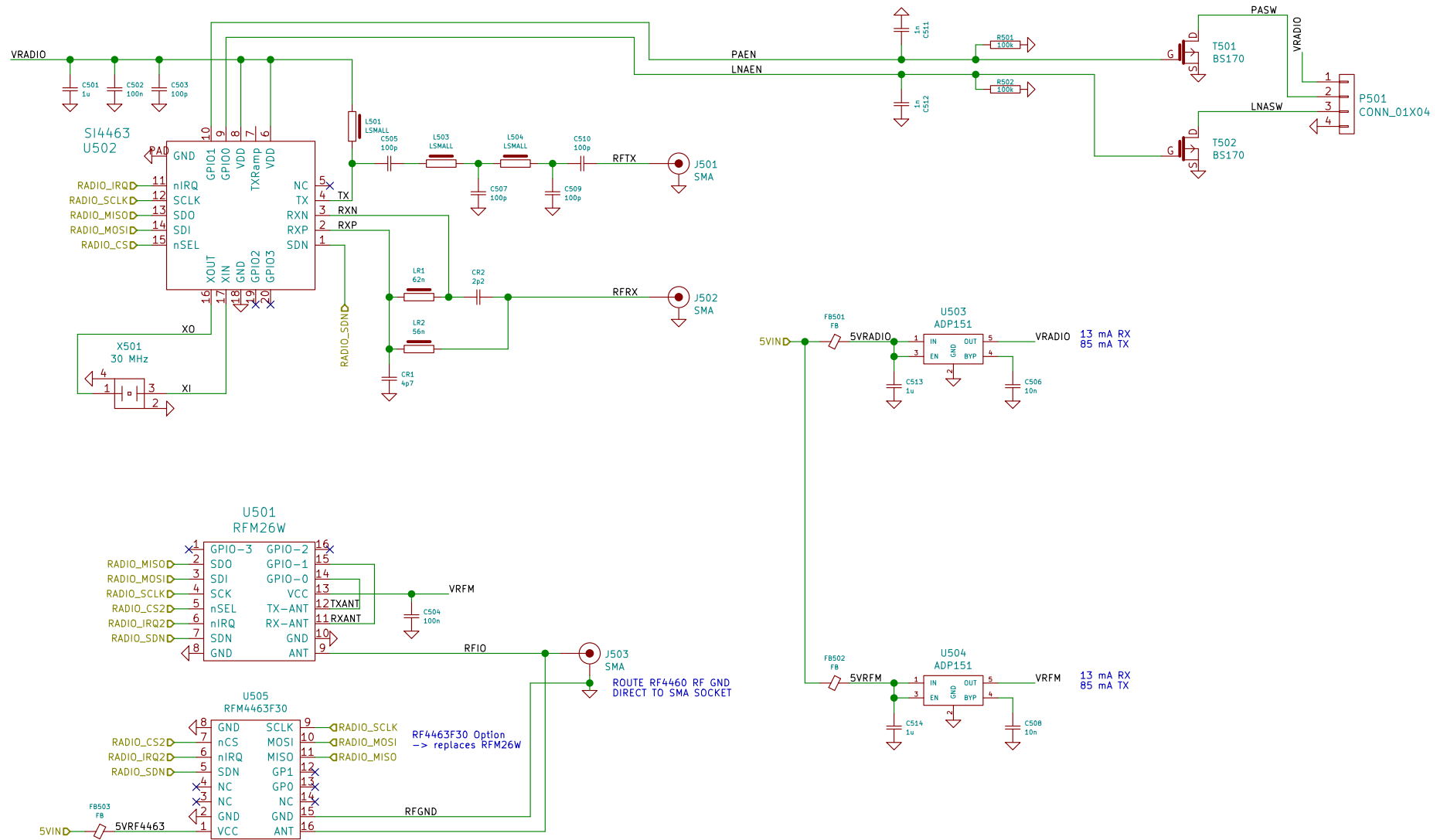
KiCad E.D.A. kicad (2017-08-12 revision 0e41632)-master

Rev: 1

Id: 4/6

FSK Radio

S501
PIN
Shielded box



Two options:
 CERNUH RFM26 module
 - direct use of si4463
F4GRX

Sheet: /radio/
 File: radio.sch

Title: Radio gateway (FSK radio)

Size: A4

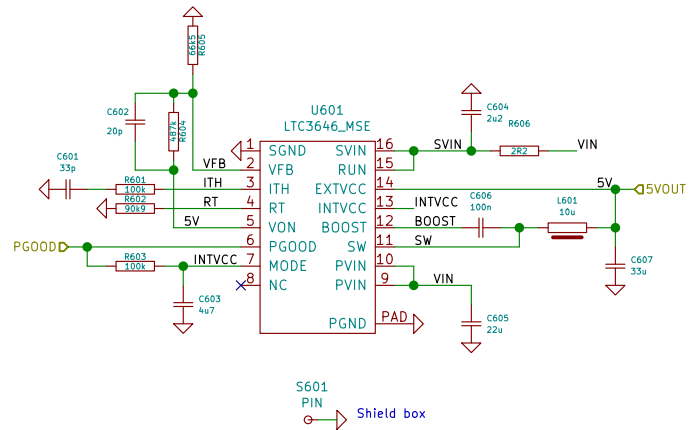
Date:

Rev: 1

KiCad E.D.A. kicad (2017-08-12 revision 0e41632)-master

Id: 5/6

Power



CERN OHL
F4GRX

Sheet: /power/
File: power.sch

Title: Radio gateway (Power supply)

Size: A4 Date: 2017-07-05

KiCad E.D.A. kicad (2017-08-12 revision 0e41632)-master

Rev: 1

Id: 6/6